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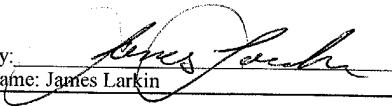
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: TAKASE ET AL. Examiner: JULIO C. GONZALEZ
Serial No.: 09/668,971 Group Art Unit: 2834
Filed: SEPTEMBER 25, 2000 Docket: 10873.574US01
Confirmation No.: 3140 Due Date: FEBRUARY 8, 2003
Title: SURFACE ACOUSTIC WAVE DEVICE AND METHOD FOR PRODUCING THE SAME

CERTIFICATE UNDER 37 CFR 1.10:

"Express Mail" mailing label number: EV 143560552 US
Date of Deposit: February 7, 2003

I hereby certify that this paper or fee is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to, Commissioner for Patents and Trademarks, Washington, D.C. 20231.

By: 
Name: James Larkin

Commissioner for Patents
Washington, D.C. 20231

Sir:

We are transmitting herewith the attached:

- Transmittal Sheet in duplicate containing Certificate of Mailing
- Amendment and Response Including Version With Markings To Show Changes Made
- Request for Extension of Time for one month and fee of \$110.00
- Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C.
P.O. Box 2903, Minneapolis, MN 55402-0903
612.332.5300

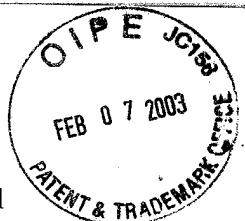


By: 
Name: Douglas P. Mueller
Reg. No.: 30,300
DPM/RAK:pll

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S/N 09/668,971



PATENT

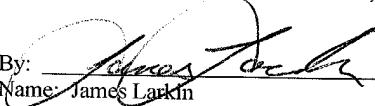
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By: 
Name: James Larkin

AMENDMENT & RESPONSE

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed on October 8, 2002, please consider and enter the following amendments and remarks. A one-month petition for extension of time is included to extend the due date for responding from January 8, 2003, to February 8, 2003.

In the Claims

Please amend claims 1 and 22 as follows.

1. (Four Times Amended) A surface acoustic wave device comprising a piezoelectric substrate, a first interdigital transducer and a second interdigital transducer formed on a surface of the piezoelectric substrate so that the first and second interdigital transducers are opposed to each other,

wherein the piezoelectric substrate includes, in the surface between the first and second interdigital transducers, a region having a lower resistance than a resistance of an inner portion of the piezoelectric substrate, and

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